

# Compare Editions

## Pro



## **Standard**



# Lite (Free)



### **Devices Supported:**

- ✓ Intel Agilex® FPGA Portfolio
- ✓ Intel® Stratix® 10 devices
- ✓ Intel<sup>®</sup> Arria<sup>®</sup> 10 devices
- ✓ Intel® Cyclone® 10 GX devices

Intel® Cyclone® 10 GX devices are supported for free in Intel® Quartus® Prime Pro Edition Software.

#### Simulation Tools

Questa\*-Intel® FPGA Edition<sup>1</sup>

### Add-On

- ✓ Intel® FPGA Power and Thermal Calculator
- ✓ Intel® FPGA AI Suite

### Additional Development Tools

- Intel® Advanced Link Analyzer Tool
- DSP Builder for Intel® FPGAs
- ✓ Intel® SoC FPGA Embedded Development Suite

### **Embedded Tools**

- Nios® V Processor, Nios® II Processor
- Arm\* Development Studio for Intel® SoC FPGA
- Ashling\* RiscFree\* IDE for Intel® FPGAs
- ✓ Intel® Simics® Simulator for Intel® FPGAs

### Devices Supported:

- ✓ Intel® Arria® 10 devices
- ✓ Arria® V FPGAs, Arria® V GZ FPGA
- Arria® II FPGAs
- ✓ Intel® MAX® 10 devices
- ✓ Intel® Cyclone® 10 LP devices
- Cyclone® V devices, Cyclone® IV FPGAs
- Stratix® V FPGAs, Stratix® IV FPGAs

The Intel® Quartus® Prime Standard Edition software includes extensive support for earlier device families.

### Simulation Tools

Questa\*-Intel® FPGA Edition

## Additional Development Tools

- Intel® Advanced Link Analyzer Tool
- ✓ Intel® HLS compiler
- DSP Builder for Intel® FPGAs
- Nios® II Embedded Design Suite
- ✓ Intel® SoC FPGA Embedded Development Suite

### **Embedded Tools**

- Nios® V Processor, Nios® II Processor
- Arm\* Development Studio for Intel® SoC FPGA
- Ashling\* RiscFree\* IDE for Intel® FPGAs

## **Devices Supported:**

- ✓ Intel® Cyclone® 10 LP devices
- Arria® II FPGAs
- ✓ Intel® MAX® 10 devices
- ✓ MAX® V CPLDs, MAX® II CPLDs

### Simulation Tools

Questa\*-Intel® FPGA Edition 1

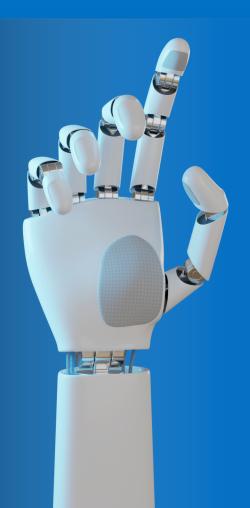
### Additional Development Tools

- DSP Builder for Intel® FPGAs

#### **Embedded Tools**

- Ashling\* RiscFree\* IDE for Intel® FPGAs





# Compare Features

## Pro



# **Standard**



# Lite (Free)



## Design Flow

- Platform Designer
- Block Based Design
- Partial Reconfiguration

### Design Entry/Planning

- Design Partition Planner
- Chip Planner
- Interface Planner
- Logic Lock Regions
- Multiprocessor Support (Faster Compile Time)
- IP Base Suite

### Placement and Routing

- Fitter (Place and Route)
- Register Retiming

### Timing and Power Verification

- Timing Analyzer
- Design Space Explorer II
- Power Analysis

### Simulation and Debug

- Signal Tap Logic Analyzer
- Transceiver Toolkit
- Questa\*-Intel® FPGA Edition Software

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Platform Designer

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Fitter (Place and Route)

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### Simulation and Debug

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i = Available for Purchase

ii. The only Arria II FPGA supported is the EP2AGX45 device.