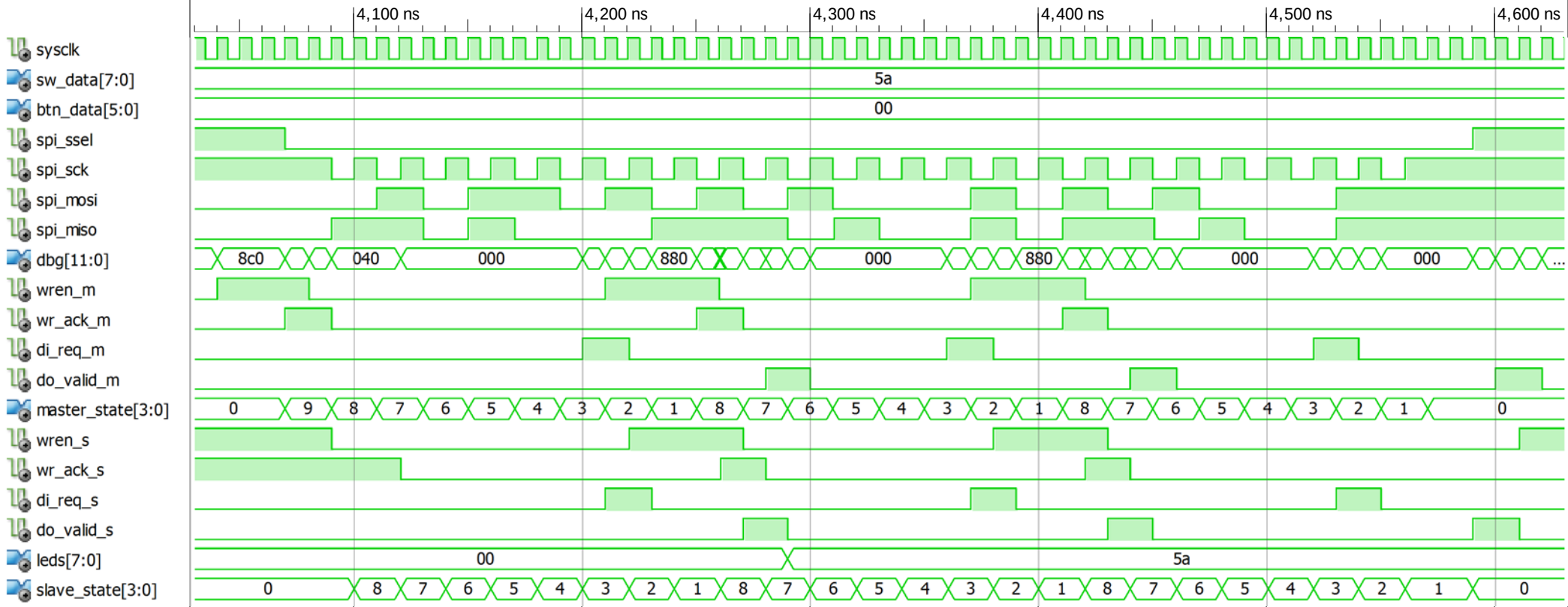
4,100 ns
4,200 ns
4,300 ns
4,400 ns
4,500 ns
4,600 ns
sysclk
sw_data[7:0]
btn_data[5:0]
spi_ssel
spi_sck
spi_mosi
spi_miso
dbg[11:0]
wren_m
wr_ack_m
di_req_m
do_valid_m
master_state[3:0]
wren_s
wr_ack_s
di_req_s
do_valid_s
leds[7:0]
slave_state[3:0]
5a
00
8c0
040
000
880
X
000
880
000
000
...
0 9 8 7 6 5 4 3 2 1 8 7 6 5 4 3 2 1 8 7 6 5 4 3 2 1 0
00
5a
0 8 7 6 5 4 3 2 1 8 7 6 5 4 3 2 1 8 7 6 5 4 3 2 1 0